

Optically controlled excitonic transistor

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Optical control of exciton fluxes is realized for indirect excitons in a crossed-ramp excitonic device. The device demonstrates experimental proof of principle for all-optical excitonic transistors with a high ratio between the excitonic signal at the optical drain and the excitonic signal due to the optical gate. The device also demonstrates experimental proof of principle for all-optical excitonic routers. © 2014 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4866855]

Excitonic devices control potential energy landscapes for excitons, exciton fluxes, emission rates, and other characteristics of excitons. Excitonic devices are used both for studies of basic properties of excitons and for development of excitonic signal processing. The potential advantages of excitonic signal processing include compact footprint and high interconnection speed. The development of excitonic devices, where exciton fluxes are controlled, is mainly concentrated on indirect excitons in coupled quantum wells (CQW)²⁻¹⁵ and exciton-polaritons in microcavities. ¹⁶⁻²⁰

An indirect exciton is a bound pair of an electron and a hole in spatially separated QW layers (Fig. 1(a)). The overlap of electron and hole wavefunctions for indirect excitons can be engineered by the structure design and applied voltage so that lifetimes of indirect excitons can exceed those of regular excitons by orders of magnitude. Long lifetimes allow indirect excitons to travel over sufficiently large distances to accommodate devices. Indirect exciton energy can be controlled by voltage: an electric field F_z perpendicular to the QW plane results in the exciton energy shift edF_z , where ed is the built-in dipole moment of indirect excitons (d is close to the distance between the QW centers for a CQW structure).²¹ Control of indirect excitons transport by voltage was demonstrated in electrostatically created in-plane potential landscapes $E(x, y) = -edF_z(x, y)$ in excitonic ramps, 2,3,15 circuit devices, 4-6,10 narrow channels, 6,8,11 traps, 7,13 lattices, 9,14 and conveyers. 12

In this work, we study indirect excitons in a crossed-ramp excitonic device. A potential energy gradient—a ramp—is created by a shaped electrode at constant voltage with no energy-dissipating voltage gradient. The design utilizes the ability to control exciton energy by electrode density. An electrode on the sample surface—the top electrode (Figs. 1(c) and 1(d))—is shaped so that a voltage applied between it and a homogeneous bottom electrode creates two crossing ramps. In each of these ramps, narrowing of the top electrode reduces F_z due to field divergence near the electrode edges and, as a result, increases the

exciton energy. The electrode shape (Figs. 1(c) and 1(d)) is

The crossed-ramp excitonic device demonstrates experimental proof of principle for all-optical excitonic transistors with optical input, output, and control gate using indirect excitons as the operation medium. Photons transform into excitons at the optical input (source) and travel to the optical output (drain) due to the ramp potential. The output signal of the exciton emission in the drain region is controlled by a gate beam.

Figures 1(e) and 1(f) show the images of the emission of indirect excitons along the turned-path and straight-path of the crossed-ramp device when only the source beam is on. The corresponding spatial profiles of the indirect exciton emission intensity I(x) are presented in Figs. 1(j) and 1(k). The profile of the source beam is shown in Fig. 1(i). The emission patterns show enhanced emission intensity around the excitation spot due to the inner-ring effect studied earlier. 23,24 The inner ring was explained in terms of exciton transport and cooling: the heating of the exciton gas by laser excitation reduces the occupation of low-energy optically active exciton states, in turn reducing the exciton emission intensity in the excitation spot. When excitons travel away from the excitation spot, they thermalize to the lattice temperature, and the occupation of low-energy optically active exciton states increases, in turn increasing the exciton emission intensity and forming photoluminescence ring around the excitation spot.^{23,24} Emission patterns also show the enhanced emission intensity of indirect excitons at the lower energy side of the ramps due to exciton transport along the potential energy gradient.

designed to obtain a constant potential energy gradient for indirect excitons in the CQW along each of the crossing ramps (Fig. 1(b)). At the crossing point, the electrodes are narrowed to compensate for the otherwise increased electrode density and thus to keep the exciton energy linear along both ramps.²² Each of the two ramps is surrounded by flat-energy channels where the electrode width and, in turn, the energy of indirect excitons are constant (Figs. 1(b)–1(d). The parameters of the structure and experimental details are presented in supplementary material.²⁵

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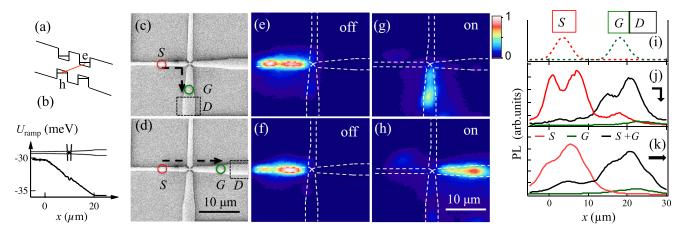


FIG. 1. (a) CQW band diagram. e, electron, h, hole. (b) Simulated potential for indirect excitons in the crossed-ramp device along one of the crossed electrodes. The electrode and a part of the other electrode near the crossing point are shown schematically in the upper part. (c) and (d) SEM images of the electrode. The shaped electrode forms the crossing ramps for indirect excitons. Red and green circles indicate the excitation spots of the source (S) and gate (G) beam, respectively. Arrows indicate the turned-path (c) or straight-path (d) operation of the excitonic transistor. (e)-(h) Images of the exciton emission in [(e) and (f)] off and [(g) and (h)] on states for the turned-path [(e) and (g)] and straight-path [(f) and (h)] transistor operation. The power of the source beam $P_S = 0.5 \,\mu\text{W}$. The power of the gate beam $P_G = 0$ [(e) and (f)] and $0.2 \,\mu\text{W}$ [(g) and (h)]. (j) and (k) Emission intensity of indirect excitons along the exciton flux for the turned-path (j) and straight-path (k) transistor operation in off state (red, $P_S = 0.5 \,\mu\text{W}$, $P_G = 0$), in on state (black, $P_S = 0.5 \,\mu\text{W}$, $P_G = 0.2 \,\mu\text{W}$), and when only the gate beam is on (green, $P_S = 0.2 \,\mu\text{W}$). (i) Spatial profiles of the source (red) and gate (green) excitation beam spots.

The regime when only the source beam is on refers to off state of the excitonic transistor. The output signal of the transistor given by the emission of indirect excitons in the drain region is weak (Figs. 1(j) and 1(k)). The exciton signal in the drain region is controlled by an optical gate. The positions of the gate beam and drain region are shown in Figs. 1(c) and 1(d) for the turned-path or straight-path operation of the excitonic transistor, respectively. Figures 1(j) and 1(k) show that turning on the gate beam strongly increases the exciton signal at the drain, switching the excitonic transistor to the on state. Figures 2(a) and 2(b) show that the on/off ratio of the output intensity in the drain region reaches two orders of magnitude. Furthermore, even a weak gate beam generating a weak exciton signal (green lines in Figs. 1(j) and 1(k)) can strongly

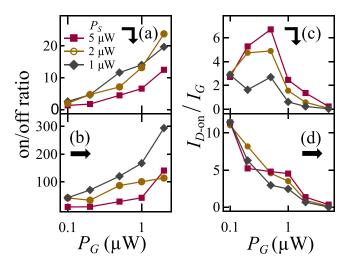


FIG. 2. (a) and (b) The contrast ratio of the output intensity integrated over the drain region. (c) and (d) The ratio of the exciton emission intensity integrated over the drain region in on state to the exciton emission intensity integrated over the entire device when only the gate beam is on. The data are shown for the turned-path [(a) and (c)] and straight-path [(b) and (d)] operation of the excitonic transistor.

increase the output in the drain. Figures 2(c) and 2(d) show that the ratio between the excitonic signal at the optical drain and the excitonic signal due to the optical gate I_{D-on}/I_G reaches an order of magnitude. We note that the excitonic signal due to the optical gate is generally smaller than the gate signal since not all of the gate photons transform to the emitted photons due to losses, therefore, in general, I_{D-on}/I_G is not equal to the transistor gain.

The earlier studied exciton optoelectronic devices, where switching was controlled by gate voltage, demonstrated a switching time below 1 ns. Since no voltage switching is required for the all-optical excitonic transistors studied here, the switching time of these devices can potentially be even shorter. The study of the signal kinetics in all-optical excitonic transistors forms the subject for future work.

The crossed-ramp excitonic device also demonstrates an experimental proof of principle of all-optical excitonic routers. In the absence of the gate beam, the output signal of exciton emission is low in both ramps after the crossing point (Figs. 1(e) and 1(f)). Positioning the gate beam on one or another ramp of the crossed-ramp device determines the path where the output signal is directed (Figs. 1(c)-1(h)). Even a weak gate beam generating a weak exciton signal can route a much stronger output signal of exciton emission (Figs. 1(j) and 1(k)).

Below we present the theoretical model for the exciton transport in a crossed-ramp device. Indirect exciton transport in a 1D potential energy channel was modeled using the following non-linear transport equation:

$$\nabla [D\nabla n + \mu n \nabla (u_0 n + U_{\text{ramp}})] - n/\tau + \Lambda_S + \Lambda_G = 0. \quad (1)$$

This equation was solved for the steady-state distribution of indirect excitons, n with $\nabla = \partial/\partial x$ (the coordinate x follows the path of the exciton flux along the energy channel and ramp). Diffusion and drift fluxes are given by the first and second terms in square brackets, respectively. The exciton diffusion coefficient D and mobility μ are related by the

generalized Einstein relationship, 23,24 $\mu = D(e^{T_0/T} - 1)$ $/(k_BT_0)$ where $T_0 = (\pi\hbar^2 n)/(2Mk_B)$ is the quantum degeneracy temperature and M is the exciton mass. The drift flux is due to the gradient in the applied ramp potential U_{ramp} (shown in Fig. 3(d)) and the exciton-exciton interaction. The exciton-exciton interaction was modeled as a repulsive dipolar interaction potential, approximated by u_0n with $u_0 = 4\pi de^2/\varepsilon_b$, where ε_b is the GaAs dielectric constant. The effect of the QW disorder potential is included in the diffusion coefficient, $D = D_0 \exp[-U_0/(u_0n + k_BT)]^{23,24}$ Here, $U_0/2 = 0.5 \,\text{meV}$ is the amplitude of the disorder potential and D_0 is the diffusion coefficient in the absence of disorder. This description includes (i) increased exciton localization to potential minima for decreasing temperature and (ii) screening of the disorder potential for increasing exciton density. 23,24

Optical generation of excitons at the source (gate) is given by $\Lambda_{S(G)}$. These have Gaussian profiles with position and FWHM chosen to match the experiment. The total injection rate of excitons is $R_{S(G)} = \delta y \int \Lambda_{S(G)}(x) dx$ where $\delta y \approx 1 \, \mu \text{m}$ is the width of the potential energy channel. The exciton optical lifetime $\tau(T_0, T)$ determines the decay of optically active excitons. ^{23,24} The spatially varying exciton temperature used in Eq. (1) is found by solving a thermalization equation

$$S_{\text{phonon}}(T_0, T) = S_{\text{pump}}(T_0, T, \Lambda_S + \Lambda_G, E_{\text{inc}}).$$
 (2)

Here, $S_{\rm phonon}$ accounts for energy relaxation of excitons due to bulk longitudinal acoustic phonon emission. $S_{\rm pump}$ is the heating rate due to the non-resonant laser excitation and is determined by the generation rates $\Lambda_{S(G)}$ and the excess energy of photoexcited excitons $E_{\rm inc}$. Expressions for $S_{\rm phonon}$, $S_{\rm pump}$, τ , and all other parameters of the model are given in Ref. 24.

The results of the simulations are presented in Fig. 3. Figure 3(d) shows the bare ramp potential U_{ramp} and the ramp potential screened by the exciton-exciton repulsion $U_{ramp} + u_0 n$. Figures 3(a)–3(c) show the emission intensity, temperature, and density of indirect excitons. Figures 3(e) and 3(f) show the contrast ratio of the output intensity in the drain region and the gain of the excitonic transistor for different source and gate powers.

Within the model, excitons generated in the gate area screen the disorder in the structure due to the repulsive dipolar interactions of indirect excitons. This increases the source exciton transport distance along the ramp, allowing excitons generated by the source laser to reach the drain region. The transport distance is further enhanced by the gate-beaminduced heating of the exciton gas due to the thermal activation of excitons in the disorder potential. The heating also increases the exciton lifetime that increases the density and, in turn, the screening of the disorder potential. The increasing exciton transport distance along the ramp generates the signal in the drain region. The features observed in the model are qualitatively similar to the experimental data, compare Figs. 1(j) and 1(k) with Fig. 3(a); Figs. 2(a) and 2(b) with Fig. 3(e); and Figs. 2(c) and 2(d) with Fig. 3(f). However, the experimentally observed ratio between the excitonic signal at the optical drain to the excitonic signal due to the optical gate is much higher than the gain of the excitonic transistor

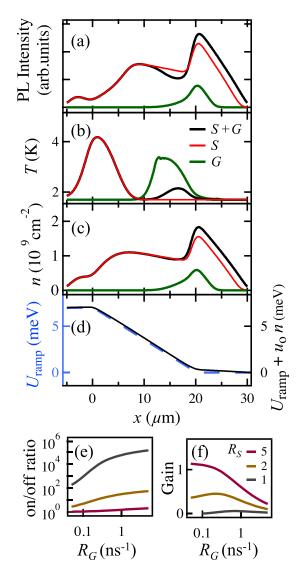


FIG. 3. Theoretical simulations: Exciton emission intensity (a), temperature (b), and density (c) for $R_S = 5 \,\mathrm{ns}^{-1}$ and $R_G = 0$ (red, off state), $R_S = 0$ and $R_G = 0.5 \,\mathrm{ns}^{-1}$ (green), and $R_S = 5 \,\mathrm{ns}^{-1}$ and $R_G = 0.5 \,\mathrm{ns}^{-1}$ (black, on state). (d) Bare ramp potential U_{ramp} (dashed blue) and screened ramp potential $U_{ramp} + u_0 n$ for $R_S = 5 \,\mathrm{ns}^{-1}$ and $R_G = 0.5 \,\mathrm{ns}^{-1}$ (black). (e) The contrast ratio of the output intensity in the drain region vs. gate generation rate R_G for different source generation rates $R_S = 1$ (black), 2 (yellow), and 5 (red) ns^{-1} . (f) The gain of the excitonic transistor. The gain is defined as the ratio of the exciton emission intensity integrated over the drain region in on state to the exciton emission intensity integrated over the entire device when only the gate beam is on.

calculated within the model. This indicates that a model beyond the considered drift/diffusion model should be developed for a quantitative description of the experimental data.

In summary, we report on experimental proof of principle for all-optical excitonic transistors with a high ratio of the excitonic signal at the optical drain to the excitonic signal due to the optical gate and experimental proof of principle for all-optical excitonic routers.

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- ²⁵See supplementary material at http://dx.doi.org/10.1063/1.4866855 for parameters of the structure, experimental details, and data for other source and gate beam wavelengths and electrode geometry.

Supplementary Materials for Optically Controlled Excitonic Transistor

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The CQW structure was grown by molecular beam epitaxy. An n^+ -GaAs layer with $n_{Si}=10^{18}$ cm³ serves as a bottom electrode. A semitransparent top electrode is fabricated by depositing a 100 nm indium tin oxide layer. Two 8 nm GaAs QWs separated by a 4 nm Al_{0.33}Ga_{0.67}As barrier are positioned 100 nm above the n^+ -GaAs layer within an undoped 1 μ m thick Al_{0.33}Ga_{0.67}As layer. Positioning the CQW closer to the homogeneous electrode suppresses the in-plane electric field¹, which otherwise can lead to exciton dissociation². Excitons are photoexcited by a 633 nm HeNe laser (data in the main text and Fig. S2 in supplementary materials) or 787.5 nm Ti:Sapphire laser (Fig. S1 in supplementary materials). The former wavelength corresponds to the photon energy above the Al_{0.33}Ga_{0.67}As barrier and the latter corresponds to the photon energy at the direct exciton resonance. Experiments are performed at $T_{bath}=1.6$ K. For the data in the main text, the voltage applied to the top electrode $V_e=-3$ V.

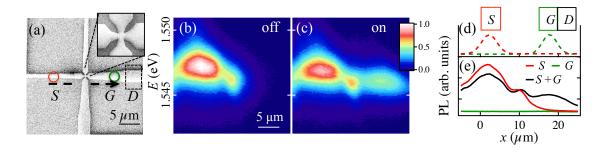


FIG. S1: (a) SEM images of the crossed-ramp device. Inset: SEM image of the crossing point of the crossed-ramp device. Red and green circles indicate the excitation spots of the source (S) and gate (G) beam, respectively. The arrow indicates the operation path of the excitonic transistor. (b,c) Energy-resolved images of the exciton emission in (b) off and (c) on states for the crossed-ramp transistor. The power of the source beam $P_S = 2 \mu W$ (b,c). The power of the gate beam $P_G = 0$ (b) and $2 \mu W$ (c). (d) Spatial profiles of the source (red) and gate (green) excitation beam spots. (e) Emission intensity of indirect excitons along the exciton flux for the crossed-ramp transistor in off state [red, $P_S = 2 \mu W$, $P_G = 0$], in on state [black, $P_S = 2 \mu W$, $P_G = 1 \mu W$], and when only the gate beam is on [green, $P_S = 0$, $P_G = 1 \mu W$]. $V_e = -4 V$.

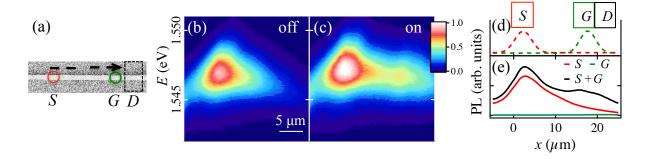


FIG. S2: (a) SEM images of the planar-electrode device. Red and green circles indicate the excitation spots of the source (S) and gate (G) beam, respectively. (b,c) Energy-resolved images of the exciton emission in (b) off and (c) on states for the planar-electrode transistor. The power of the source beam $P_S = 10 \,\mu\text{W}$ (b,c). The power of the gate beam $P_G = 0$ (b) and $2 \,\mu\text{W}$ (c). (d) Spatial profiles of the source (red) and gate (green) excitation beam spots. (e) Emission intensity of indirect excitons for the planar-electrode transistor in off state [red, $P_S = 10 \,\mu\text{W}$, $P_G = 0$], in on state [black, $P_S = 10 \,\mu\text{W}$, $P_G = 2 \,\mu\text{W}$], and when only the gate beam is on [green, $P_S = 0$, $P_G = 2 \,\mu\text{W}$]. $V_e = -4 \,\text{V}$.

Qualitatively similar data are measured for the source and gate beam wavelengths 633 nm (main text) and 787.5 nm (Fig. S1). Furthermore, qualitatively similar data are measured for the cross-ramp device (main text) and planar-electrode device, which forms a flat-energy channel (Fig. S2). In all these cases (i) the gate beam strongly increases the exciton signal at the drain region and (ii) even a gate beam, which generates a weak exciton signal, can strongly increase the output at the drain (Figs. 1 and 2 in the main text and Figs. S1 and S2). This indicates that all-optical excitonic transistors can operate at various source and gate beam wavelengths and various electrode geometries.

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